

SIMPlugIN-LX45 support files for memory test

The support files present an implementation of a memory test in SIMPlugIN-LX45 board. It is just an example so it does not come with any warranty whatsoever. It is just a quick made example to demonstrate functionality of the board.

It was compiled with Xilinx ISE rev 13.1 using the capabilities SDK and EDK that are NOT free of cost (you can download free ISE rev 13.1 but to use SDK and EDK functionalities it is necessary to buy a license).

Due to the above reason and the fact the using microBlaze embedded processor (so EDK and SDK) is not simple only a partial source code is provided.

The jumpers must be configured as stated by default in the board user manual.

PUSHB0 initializes the test.

Using miniusb CON13 connector you connect to a PC where you must run Hyperterminal (or similar software) that emulates a terminal. Configure to 19200 baud, 8 bit , no parity, 1 stop bit.

In the terminal you will see the result of the continuous test:

```
--Starting Memory Test Application--
NOTE: This application runs with D-Cache disabled.As a result, cacheline requests
will not be generated
Testing memory region: mpmc_0
  Memory Controller: mpmc
    Base Address: 0x48000000
      Size: 0x08000000 bytes
    32-bit test: PASSED!
    16-bit test: PASSED!
    8-bit test: PASSED!
--Memory Test Application Complete--
```